Electronic Pate	ent App	lication Fee	Transm	ittal			
Application Number:	100	10699707					
Filing Date:	03-	03-Nov-2003					
Title of Invention:	REG	RECONFIGURABLE CHIP LEVEL EQUALIZER ARCHITECTURE					
First Named Inventor/Applicant Name:	An	Antonio F. Mondragon-Torres					
Filer:	Ro	Ronald O. Neerings/Elizabeth Austin					
Attorney Docket Number:	TI-3	TI-35731					
Filed as Large Entity	1						
Utility under 35 USC 111(a) Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Utility Appl issue fee		1501	1	1740	1740		

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
	Total in USD (\$)			2040